

## CLAIMS

What is claimed is:

1. A system, comprising:
  - a first write state machine;
  - a second write state machine;
  - a pulse generator operable to generate a first pulse of current to the first state write machine and a second pulse of current to the second write state machine; and
  - a delay circuit operable to inject a time delay between the first pulse of current and the second pulse of current.
2. The system of Claim 1, wherein the pulse generator is operable to generate a plurality of pulses of current having a predetermined waveform, wherein the waveform has a large initial pulse of current followed by a plurality of smaller pulses of current.
3. The system of Claim 2, wherein the delay circuit is operable to delay the second pulse of current which is applied to the second write state machine for at least as long as the duration of the first initial pulse of current which is applied to the first write state machine.
4. The system of Claim 2, wherein the second pulse of current which is applied to the second write state machine occurs during a delay between the first initial pulse of current applied to the first write state machine and the plurality of smaller pulses of current applied to the first write state machine.

5. The system of Claim 2, wherein the second pulse of current which is applied to the second write state machine occurs during a delay between a first plurality of smaller pulses of current applied to the first write state machine and a second plurality of smaller pulses of current applied to the first write state machine.

6. The system of Claim 2, wherein the first pulse of current which is applied to the first write state machine has an amplitude substantially equal to the amplitude of the second pulse of current which is applied to the second write state machine.

7. The system of Claim 2, wherein the plurality of additional pulses have amplitudes that are less than or equal to half of the amplitude of the first pulse of current applied to the first write state machine.

8. A method, comprising:

applying a first pulse of current to a first write state machine;

delaying a second pulse of current by a predetermined amount of time from the first pulse; and

applying the second pulse of current to a second write state machine.

9. The method of Claim 8, wherein the pulse generator is operable to generate a plurality of pulses of current having a predetermined waveform, wherein the waveform has a large initial pulse of current followed by a plurality of smaller pulses of current.

10. The method of Claim 9, wherein the first pulse of current which is applied to the first write state machine has an amplitude substantially equal to the amplitude of the second pulse of current which is applied to the second write state machine.

11. The method of Claim 9, wherein the predetermined amount of time for the delay is at least as long as the duration of the first pulse of current applied to the first write state machine.

12. The method of Claim 9, wherein the second pulse of current which is applied to the second write state machine occurs during a delay between the first initial pulse of current applied to the first write state machine and the plurality of smaller pulses of current applied to the first write state machine.

13. The method of Claim 9, wherein the second pulse of current which is applied to the second write state machine occurs during a delay between a first plurality of smaller pulses of current applied to the first write state machine and a second plurality of smaller pulses of current applied to the first write state machine.

14. The method of Claim 9, wherein the plurality of additional pulses have amplitudes that are less than or equal to half of the amplitude of the first pulse of current applied to the first write state machine.

15. A computer-readable medium having computer-executable instructions, comprising:  
applying a first pulse of current to a first write state machine;

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delaying a second pulse of current by a predetermined amount of time from the first pulse; and

applying the second pulse of current to a second write state machine.

16. The computer-readable medium of Claim 15, wherein the pulse generator is operable to generate a plurality of pulses of current having a predetermined waveform, wherein the waveform has a large initial pulse of current followed by a plurality of smaller pulses of current.

17. The computer-readable medium of Claim 16, wherein the first pulse of current which is applied to the first write state machine has an amplitude substantially equal to the amplitude of the second pulse of current which is applied to the second write state machine.

18. The computer-readable medium of Claim 16, wherein the predetermined amount of time for the delay is at least as long as the duration of the first pulse of current applied to the first write state machine.

19. The computer-readable medium of Claim 16, wherein the second pulse of current which is applied to the second write state machine occurs during a delay between the first initial pulse of current applied to the first write state machine and the plurality of smaller pulses of current applied to the first write state machine.

20. The computer-readable medium of Claim 16, wherein the second pulse of current which is applied to the second write state machine occurs during a delay between a first plurality of

smaller pulses of current applied to the first write state machine and a second plurality of smaller pulses of current applied to the first write state machine.

21. The computer-readable medium of Claim 16, wherein the plurality of additional pulses have amplitudes that are less than or equal to half of the amplitude of the first pulse of current applied to the first write state machine.